

Docket No. 240708US-2  
IN RE APPLICATION OF: Kenichi HAYASHI, et al.  
SERIAL NO: 10/625,583  
FILED: July 24, 2003  
FOR: SEMICONDUCTOR DEVICE AND SEMICONDUCTOR ASSEMBLY MODULE



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COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

SIR:  
Transmitted herewith is an amendment in the above-identified application.

- ☐ No additional fee is required
- ☐ Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.
- ☒ Additional documents filed herewith: Petition for Extension of Time (1 month), Letter Submitting Replacement Drawing Sheet(s), Replacement Sheets (2)

The Fee has been calculated as shown below:

CLAIMS	CLAIMS REMAINING		HIGHEST NUMBER PREVIOUSLY PAID	NO. EXTRA CLAIMS	RATE	CALCULATIONS
TOTAL	20	MINUS	20	0	x \$50 =	\$0.00
INDEPENDENT	1	MINUS	3	0	x \$200 =	\$0.00
		<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS			+ \$360 =	\$0.00
		TOTAL OF ABOVE CALCULATIONS				\$0.00
		<input type="checkbox"/> Reduction by 50% for filing by Small Entity				\$0.00
		<input type="checkbox"/> Recordation of Assignment			+ \$40 =	\$0.00
		TOTAL				\$0.00

- ☐ A check in the amount of \$0.00 is attached.
- ☒ Credit card payment form is attached to cover the fees in the amount of \$120.00
- ☒ Please charge any additional Fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.
- ☒ If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

*Surinder Sachar*

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Registration No. 34,423



Docket No.: 240708US2

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

IN RE APPLICATION: Kenichi HAYASHI, et al.

SERIAL NO.: 10/625,583

GAU: 2826

FILED: July 24, 2003

EXAMINER: Williams, A.

FOR: SEMICONDUCTOR DEVICE AND SEMICONDUCTOR ASSEMBLY MODULE

**LETTER SUBMITTING REPLACEMENT DRAWING SHEET(S)**

COMMISSIONER FOR PATENTS  
Alexandria, VA 22313

SIR:

Responsive to the below indicated communication, the following drawing sheets are submitted herewith:

☒ 2 Replacement Drawing Sheets ☐ \_\_\_\_\_ New Drawing Sheets

☒ Official Action dated December 28, 2004

☐ Notice of Allowance/Issue Fee dated \_\_\_\_\_

☐ Other dated \_\_\_\_\_

The changes and/or modifications made include the following:

The attached sheets of drawings include changes to Figs. 23A-23D and Fig. 24. These sheets which have been amended to add the words "Prior Art" to Figs. 23A-23D and Fig. 24, replace the original sheets including Figs. 23A-23D and Fig. 24.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

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